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Application Number 10/625,264

Filing Date July 23, 2003

First Named Inventor Garrett

Art Unit Unknown

Examiner Name Unknown

Attorney Docket Number RAMB-01048US1

			U. S. PATENT	DOCUMENTS	
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ^{2 (* known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US- 3,646,329*	02-29-1972	Yoshino et al.	
	 	US- 4,192,005*	03-04-1980	Kurtz	
	<u> </u>	US- 4,315,210*	02-09-1982	Michel et al.	
	<u> </u>	^{US-} 4,329,600*	05-11-1982	Stewart	
		US-4,414,480*	11-08-1983	Zasio	
	1	^{US-} 4,549,124*	10-22-1985	Beier	
		US- 4,680,487*	07-14-1987	Kobayashi	
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		^{US-} 4,707,620*	11-17-1987	Sullivan et al.	
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Examiner Initials*	Cite No.1	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages	
		Country Code ³ Number ⁴ Kind Code ⁵ (if known)	MM-DD-YYYY	,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,,	Or Relevant Figures Appear	Τ ⁶
		EP0482392*	02-1991			
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Application Number	10/625,264			
Filing Date	July 23, 2003			
First Named Inventor	Garrett			
Art Unit	Unknown			
Examiner Name	Unknown			
Attorney Docket Number	RAMB-01048US1			

			U. S. PATENT	DOCUMENTS	
Examiner Initials*	Cite No. ¹	Document Number Number-Kind Code ^{2 (# known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		^{US-} 4,860,198*	08-22-1989	Takemaka	-
	<u> </u>	^{US-} 4,894,562*	01-16-1990	Cavaliere et al.	-
		^{US-} 4,939,389*	07-3-1990	Cox et al.	
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		us- 5,097,148*	03-17-1992	Gabara	
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		Country Code ³ -Number ⁴ -Kind Code ⁵ (if known)			Of Relevant Figures Appear	L <u>'</u>
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Application Number 10/625,264

Filling Date July 23, 2003

First Named Inventor Garrett

Art Unit Unknown

Examiner Name Unknown

Attorney Docket Number RAMB-01048US1

	_		U. S. PATENT	DOCUMENTS	
Examiner Initials*	Cite No.1	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ^{2 (if known)}			Figures Appear
		^{US-} 5,107,230*	04-21-1992	King	
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		^{US-} 6,442,644	08-27-2002	Gustavson et al.	
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		FORE	IGN PATENT DOCU	MENTS		
Examiner Initials*	Cite No.1	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages	
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Sheet	4	of	5	Attorney Docket Number	RAMB-01048US1

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Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		*Reynolds, C.B., "Analysis and Guidelines for High-Speed VLSI System Interconnections," IEEE 1988 Custom Integrated Circuits Conference, pp. 23.5.1-23.5.4.	
		*Leung, Kam, "Controlled Slew Rate Output Buffer," IEEE 1988 Custom Integrated Circuits Conference, pp. 5.5.1-5.5.4.	
		*Knight, Thomas F. Jr., and Krymm, Alexander, "A Self-Terminating Low-Voltage Swing CMOS Output Driver," Journal of Solid State Circuits, Vol.23, No.2, Apr. 1998, pp. 457-464.	
		*Cox, Dennis T., et al.,"VLSI Performance Compensation for Off-Chip Drivers and Clock Generation," IEEE 1989 Custom Integrated Circuits Conf., pp.14.3.1-14.3.4.	
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-		*"Driver with Noise-Dependent Switching Speed Control," IBM Technical Disclosure Bulletin, Vol. 29, No. 3, Aug. 1986, IBM Corporation, pp. 1243-1244.	
	_	*Biber, Alice I. "The Design of an Application Specific Interface Drive for a High Capacitive Load," Masters Thesis at the Massachusetts Inst. of Technology, Dec. 1989.	
		*Raver, Norman, "Open-Loop Gain Limitations for Push-Pull Off-Chip Drivers," Journal of Solid-State Circuits, Vol. SC-22, No. 2, Apr. 1987, pp.145-150.	
		*Senbon, T. and Hanabuchi, F., Instrumentation Systems: Fundamentals and Applications, Chapter 3: Detection and Conversion of Industrial Variables; 1991, 11 pages.	
		*Johnson, C. and Richeh, H, "Highly Accurate Resistance Deviation to Frequency Converter with Programmable Sensitivity and Resolution," IEEE vol. IM-35, No.2, Jun.1986, 4 pages.	

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INFORMATION DISCLOSURE				Filing Date	July 23, 2003	
STATEMENT BY APPLICANT			PPLICANT	First Named Invent r	Garrett	
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		*Sasaki, H., et al., "High Precision Automated Resistance Measurement Using A Modified Wheatstone Bridge," CPEM '88 Digest, Japan, 1988, 1 page.			
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		*Armstrong, David H., "Pitfalls in Testing Digital ASIC Devices," IEEE 1987 Custom Integrated Circuits Conference, pp. 573-578.			
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	/	Gillingham, Peter, and Vogley, Bill, "SLDRAM" High-Performance, Open-Standard Memory", 1997 IEEE, November/December 1997, pp. 29-39			
	/	Draft 0.99 IEEE P1596.7-199X, "Draft Standard for A High-Speed Memory Interface (SyncLink)", Oct. 14, 1996 IEEE			

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